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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Fernando Gonzalez et al.

Serial No.: 10/751,141

Filed: December 31, 2003

For: Transistor Having Vertical Junction Edge and Method of Manufacturing the Same

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING
37 C.F.R. 1.8

RESPONSE TO RESTRICTION REQUIREMENT

In response to the Office Action (Restriction) mailed December 10, 2004, please amend the above-identified application as follows: